原厂授权一级代理商(有正规代理证书) 深圳市诚信联科技有限公司 联系人:古/S 13249827170 QQ:1262314675/1139196806 2017/02/06



# High Constant Current LED Driver Datasheet

NU511

#### **1.2A Single Channel LED Driver**

#### Features

- 100mA~1.2A, single channel constant current regulator
- Output current adjustable by external resistor
- 3V ~ 12V wide range supply voltage
- 1MHz OE dimming support
- 400Hz V<sub>DD</sub> dimming support
- 0V ~ 17V output sustain voltage
- low output voltage dropout
  0.2V dropout at 150mA output
  0.6V dropout at 1.2A output
- Minimized I<sub>DD</sub> consumption
- 160°C half power thermal protect
- Less than ±4% chip current skew
- Less than 0.5%/V line regulation
- Less that 1%/V load regulation
- Green package

## **Product Description**

The NU511 is a single channel, open drain, linear constant current LED driver. With up to 1.2A driving current and maximum 1Mhz PWM dimming control support, NU511 can be used for 0.5W to 3W high power LED string in general or architecture decoration lighting applications.

The wide power supply range capability makes NU511 work stably in uncertain power supply applications. When the power supply voltage is changed or fluctuating, the output current still remains unchanged. With this dedicate designed function, the  $V_{\rm DD}$  power can be derived easily from the whole lighting system.

The output current of NU511 is set by an external resistor. While in full current output, the NU511 only need about 0.6V drop on output channel. The minimized voltage drop will increase the working range that limited by LED forward voltage variation in lighting system, to enhance the system efficiency and lower the heat generation from NU511.

#### **Applications**

- General LED Lighting
- Decoration lighting for architecture
- LCD back lighting
- Street lamp

## **Package Type**

 TO252-5L (Part No. : NU511T2)



• MSOP 8

1		1
VDD	•	GND
OE 🗖	UN	Rext
OPT 📃	51	NC
NC	-	NC

## **Terminal Description**

Pin name	Function
REXT	R external
OUT	Output
Gnd	Ground
V <sub>DD</sub>	Power
OE	Output enable

## **Protection Circuit**

8KV output channel ESD protection

# Maximum Ratings (T = $25^{\circ}$ C)

Characteristic	Symbol		Rating	Unit	
Supply voltage	V	DD	3.0 ~ 16	V	
Output voltage	V <sub>OUT</sub>		-0.2 ~ 20	V	
Input voltage	V	OE	$-0.2 \sim V_{\text{DD}}$	V	
Output current	١ <sub>c</sub>	UT	1.4	А	
Ground terminal current	I <sub>GND</sub>		1.4	А	
Power Dissipation (On PCP)	D-	TO252	3.2	۱۸/	
	PD	MSOP8	0.7	vv	
Thormal Posicianco	D	TO252	42	°C /\\/	
	nth(j-a)	MSOP8	240	C/W	
Operating temperature	T <sub>OPR</sub>		-40 ~ +130	°C	
Storage temperature	T	STG	-55 ~ +150	°C	

# **Electrical Characteristics and Recommended Operating Conditions**

Characteristic	Symbol	Condition		Min.	Тур.	Max.	Unit
Supply voltage	V <sub>DD</sub>	Room Temp.		3	-	12	V
Supply voltage surge/ripple peak to peak limit	$V_{\text{DD-PP}}^{*1}$	3V < V <sub>D</sub>	<sub>D</sub> < 12V	-	-	2	V
Output sustain voltage	Vomax	lout	= 0A	-	-	17	V
Output current	Іоит			100	-	1200	mA
		V <sub>DD</sub> = 5V, Io	υт <b>= 150mA</b>	0.2	-	-	V
Output drop out voltage	Vout	V <sub>DD</sub> = 5V, Io	ut = 350mA	0.3	-	-	V
		$V_{DD} = 5V, I_{OL}$	<sub>JT</sub> = 1200mA	0.6	-	-	V
Dowor dissinction	D	(Ta=25°C)	TO252	-	-	1.8	14/
Power dissipation	PD	(1a-25 C)	MSOP8	-	-	0.45	vv
Chip to chip current skew V <sub>DD</sub> >=3V	dI <sub>OUT2</sub>	I <sub>OUTn</sub> = 1200mA		-	±2	±3.5	%
Leakage	Leakage	V <sub>OUT</sub> = 7V		-	-	1	uA
	V	V <sub>DD</sub> <		-	0.7*V <sub>DD</sub>	-	
	V <sub>IH</sub>	V <sub>DD</sub> >	V <sub>DD</sub> >= 5V -	-	3.5	V	
OE input voltage	V	VDD	< 5V	-	0.3*V <sub>DD</sub>	-	1
	VIL	V <sub>DD</sub> >	= 5V	-	-	1.5	
Pull down resistor (OE)	$R_{PD}$	On small	area PCB	400	500	700	KΩ
Line regulation	%/V <sub>DD</sub>	3V < V <sub>D</sub>	<sub>D</sub> < 12V	-	-	0.5	<b>∆%/</b> V
Load regulation	%/Vout	0.5V < V <sub>OUT</sub> < 8V		-	-	1	<b>∆%/V</b>
Operating Temperature	T <sub>OPR</sub>	Ambient temperature		-40	-	85	°C
Thermal regulation	% <b>/10</b> ℃			-	-	0.5	<b>∆%/10</b> ℃
Thermal protect (Junction temperature)	$T_{HalfP}$	Half curre	nt output	-	160	-	°C

- 2 -Please visit <u>http://www.numen-tech.com</u> for more patent information. 3F., No. 96, Sec. 1, Jiafong 6<sup>th</sup> Road, Jhubei City, Hsinchu County 302, Taiwan.

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
		R <sub>EXT</sub> = Open, Output off	-	0.3	1	mA
Supply surrent		Iout = 365mA, Output on	2	4	4.5	mA
Supply current	t IDD	Iout = 700mA, Output on	-	4.7	-	mA
		Iout = 1000mA, Output on	-	5.0	-	mA
System voltage	VLED	$V_{DD} < 12V$	5	-	24	V

\*1 The surge or ripple resided in V<sub>DD</sub> supply voltage peak to peak (V<sub>DD-PP</sub>) should be less than 2V. The higher surge or ripple will damage or lower the reliability of NU511. To avoid this damage, a 4V or 5V Zener diode can be connected to V<sub>DD</sub> pin to clamp the supply voltage variation.

## Switching Characteristics (T = 25°C)

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
Propagation Delay Time (OE from "L" to "H")	t <sub>pLH</sub>	$V_{DD}=4V, V_{OUT}=1V,$ Iout=120mA, OE= 0V $\rightarrow$ 4V	200	280	360	nS
Output current rising time (OE from "L" to "H")	toerise	$V_{DD}=4V, V_{OUT}=1V,$ Iout=120mA, OE= 0V $\rightarrow$ 4V	30	50	80	nS
Propagation Delay Time (OE from "H" to "L")	t <sub>рнL</sub>	$V_{DD}=4V, V_{OUT}=1V,$ I <sub>OUT</sub> =120mA, OE= 4V $\rightarrow$ 0V	560	620	680	nS
Output current falling time (OE from "H" to "L")	toefall	$V_{DD}=4V, V_{OUT}=1V,$ $I_{OUT}=120mA, OE=4V \rightarrow 0V$	60	90	130	nS
Propagation Delay Time ( $V_{DD}$ from "L" to "H")	t <sub>pON</sub>	V <sub>OUT</sub> =1V, I <sub>OUT</sub> =120mA, V <sub>DD</sub> =OE= 0V $\rightarrow$ 3V	-	30	-	uS
Output current rising time (VDD from "L" to "H")	tvDDRise	V <sub>OUT</sub> =1V, I <sub>OUT</sub> =120mA, V <sub>DD</sub> =OE= 0V $\rightarrow$ 3V	-	5		uS
Propagation Delay Time ( V <sub>DD</sub> from "H" to "L")	tpoff	$V_{OUT}=1V$ , $I_{OUT}=120mA$ , $V_{DD}=OE=3V \rightarrow 0V$	-	3	-	uS
Output current falling time (V <sub>DD</sub> from "H" to "L")	t <sub>vDDFall</sub>	$V_{OUT}=1V$ , $I_{OUT}=120$ mA, $V_{DD}=OE=3V \rightarrow 0V$	-	5		uS

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## **Timing Waveform**



OE timing diagram



 $V_{DD}$  timing diagram ( $V_{DD}=V_{OE}$ )

# Test Circuit





## **Block Diagram**



# **Equivalent Circuits for Inputs**

There is only one OE input terminal to which a pull down resistor is connected. While OE is high voltage, the output channel is turned on.



# **Output Current Setting**

The output current of each channel of NU511 is set by an external resistor ( $R_{EXT}$ ). The relationship between output current and external resistor is shown in the figure or calculated from the equation following.



#### **I/V Curve**





NU511

# **Typical Application Circuit**



• General lighting application 2



#### Note:

1. For the heat consideration on driver, V<sub>OUT</sub> of NU511 should be minimized. The power calculation equation is shown as bellow.

 $V_{OUT} = VLED - V_F * n$  $P_D = V_{OUT} * I_{OUT}$ 

Where  $V_{OUT}$  is the voltage on output pins,  $I_{OUT}$  is output current of NU511,  $V_F$  is voltage drop of LED and **n** is the number of LEDs. In some higher  $V_{OUT}$  applications, to series a proper resistor in output current path can decrease the  $V_{OUT}$  and get less heat generation from NU511.

- 2. For the efficiency consideration, higher VLED voltage and more LEDs in current path will get higher electrical efficiency. With the wide range supply voltage design and self powering structure like the lighting application circuit on previous page, NU511 can be used in maximum 24V (VLED) power system.
- 3. More LED in series, the total voltage drop variation on LEDs will increase. This variation is derived from the different V<sub>F</sub> bins of LEDs and LED temperature rising while system is working. That probably increases P<sub>D</sub>. So, it is another trade off to select the proper VLED voltage and the number of LEDs in system. The more output current is driving, the less LED in series is better.



#### **Power Dissipation and Recommand Iout-Vout Table**

Iout (A)	Max. Vout (V) recommended
0.1	17
0.2	9
0.3	6
0.4	4.5
0.5	3.6
0.6	3
0.7	2.57
0.8	2.25
0.9	2
1	1.8
1.1	1.64
1.2	1.5

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## **IR Reflow**

Line Speed:33 cm/min



# **Soldering Pattern**

• TO252-5L

5.7 7 2.3 0.6 0.6 0.67 0.67 • MSOP8



Unit:mm Tolerance:+/-0.1

## **Package Dimensions**

• TO252-5L









	DIMENSION	IS IN INCH	DIMENSIONS	n millimeter
SIMDOLS	MIN,	MAX.	MIN.	MAX.
А	0.086	0.094	2,18	2.39
A1	0.000	0.005	0.00	0.13
A2	0.040	0.050	1.02	1 <i>.</i> 27
b	0.020	) TYP.	0.51	TYP.
b2	0.205	0.215	5.21	5 <i>.</i> 46
С	0.018	0.023	0.46	0.58
c1	0.018	0.023	0.46	0.58
D	0.210	0.220	5.33	5.59
D1	0.180	_	4.57	-
E	0.250	0.265	6.35	6.73
E1	0.150	_	3.81	-
e	0.050	D BSC.	1.27	BSC.
Н	0.370	0.410	9.40	10,41
L	0.055	0.070	1.40	1 <i>.</i> 78
L1	0.105 REF.		2.67	REF.
L2	0.06	0.08	1.52	2.03
θ	0.	4 <b>*</b>	0°	4'

#### NOTES:

1. JEDEC OUTLINE : N/A

#### **Taping Specification**

Part No.	PACKAGE	Q'TY/REEL
NU511T2	TO252-5	3,000 ea

#### • MSOP-8



SYMBOLS	DIMENSIONS IN MILLIMETER			
SIMDOLS	MIN	NOM	MAX	
А			1.10	
A1	0.00		0.10	
A2	0.75	0.85	0.95	
b	0.22	0.30	0.38	
С	0.13	0.15	0.23	
D		3.00BSC		
E		4.90BSC		
E1		3.00BSC		
e		0.65BSC		
L	0.40	0.53	0.66	
У			0.10	
θ	0°		6°	
L1	0.85	0.95	1.05	

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