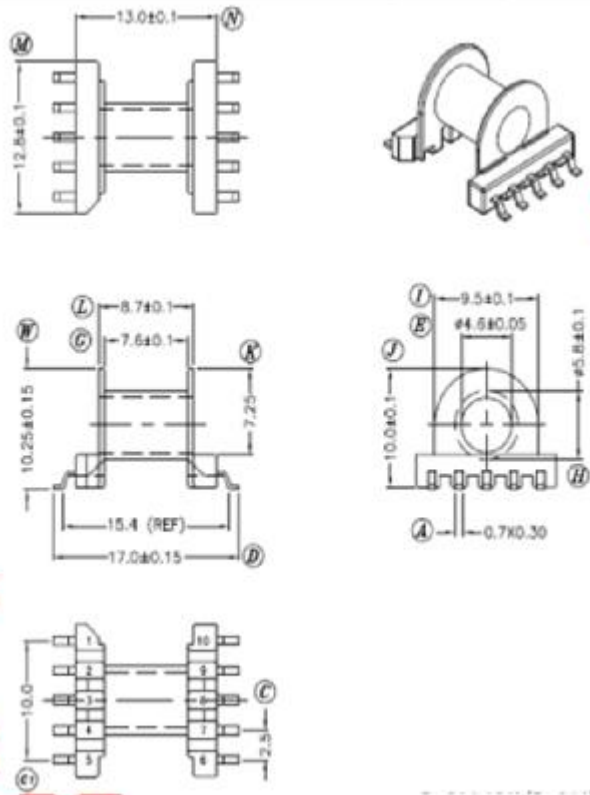
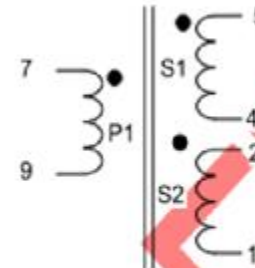


Mechanical drawing



PCB layout



Request for a gate drive transformer

Topology	Gate drive transformer
Ratio:	1:1:1
Creepage distance	Primary/secondary = 7mm min Secondary/secondary = 1.3mm min
Switching frequency:	100kHz
Duty cycle:	50%
Maximum peak Current per winding:	$I_{pic} S1 = S2 = 4.5A$ / $I_{eff} S1=S2 = 0.185A_{eff}$ Typ load = 3nF@12V with 2.7ohm
Nominal DC current per secondary winding:	#0A
Primary voltage	Typ = +/-12V (15V max)

secondary voltage	Typ = +/-12V (15V max)
Dielectric insulation between winding :	P1/(S1 and S2) = 3kVac S1/S2 = 1kVac
Leakage inductance between winding	0.3µH typ (0.5µH max)
Parasitic capacitor between primary/secondary	TBD
Temperature rise at nominal power:	30°C max