

1.5A, Hysteretic, High Brightness LED Driver with Internal Switch

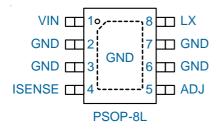
General Description

The NT3902 is a continuous mode inductive step-down and buck-boost converter, designed for driving single or multiple series connected LEDs from a voltage source higher than or lower than the LED voltage. It operates from input supply between 7V and 40V and provides and externally adjustable output current up to 1.5A with proper supply voltage and external components,the NT3902 can provide more than 50 watts of output power .

This NT3902 includes the output switch and high-side output current sensing circuit, which uses an external resistor to set the nominal average output current.

The ADJ pin will accept either a wide range pulse dimming waveform or a DC voltage. This will provide either a continuous or gated output current depending upon the control frequency. The soft-start time can be increased by connecting an external capacitor from the ADJ pin to ground. The PWM filter components also serve as soft-start time setting. Applying a 0.2V or lower to the ADJ pin turns the output off and switches the device into a low current standby state.

■ Pin Configuration



Features

- □ Operates from 7V to 40V Supply Voltage
- □ Single Pin On/Off and Brightness Control Using DC Voltage or PWM
- Up to 95% Efficiency
- □ Internal 40V NDMOS Switch
- Up to 1.5A Output Current
- □ Typical +/-5% LED Current Accuracy
- Input Under Voltage Lockout
- PSOP-8L Package
- RoHS Compliant and Halogen-Free

.Applications

- Automatic LED Lighting
- ☐ High Power LED Lighting
- Indicator and Emergency Lighting
- Architectural Lighting
- Low Voltage Industrial Lighting
- Signage and Decorative LED Lighting

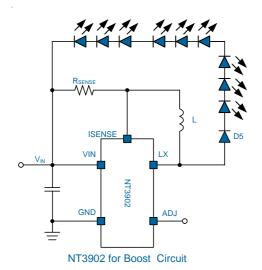
Ordering Information

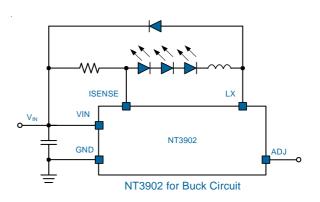
Order Number	Package	Top Marking
NT3902PSW8	PSOP-8L	NT3902P

Note: NT products are compatible with the current IPC/ JEDEC J-STD-020 requirement. They are halogen-free, RoHS compliant and 100% matte tin (Sn) plating that are suitable for use in SnPb or Pb-free soldering processes.



Typical Application Circuit



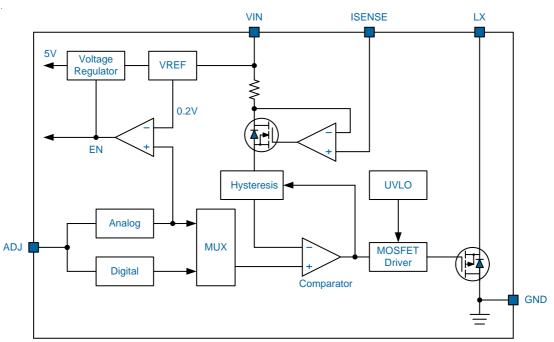


Functional Pin Description

Pin No.	Pin Name	Pin Function	
1	VIN	Power Supply Input Pin. Connect a nominal (7V~40V) power supply to this pin. The power-on-reset (POR) function monitors the input voltage by this pin. It is recommended that a decoupling capacitor (4.7uF or Higher X7R Ceramic capacitor) be connected to the GND for noise decoupling.	
2/3/6/7	GND	Ground for the IC. All voltages levels are measured with respect to this pin.	
4	ISENSE	Current Sense Pin: Sense LED String Current.	
5 ADJ		Multi-Function ON/OFF and Brightness Control Pin: - Analog signal input for analog control of PWM dimming PWM signal input for digital PWM dimming.	
8	LX	Drain of NMOS Switch	
Expose Pad		Ground and Thermal Pad.	



Functional Block Diagram



Functional Description

The NT3902 is a simple high-efficiency, continuous mode inductive step-down converter. The device operates with an input voltage range from 7V to 40V and delivers up to 1.5A of output current. A high-side current-sense resistor sets the output current and a dedicated PWM dimming input enables pulsed LED dimming over a wide range of brightness levels. A high side current-sensing scheme and an on-board current setting circuitry minimize the number of external components which is required while delivering LED current with ±5% accuracy, using a 1% sense resistor.

Adjusting Output Current

The device contains a low pass filter between the ADJ pin and the threshold comparator and an internal current limiting resistor between ADJ and the internal reference voltage. This allows the ADJ pin to be overdriven with either DC or pulse signals to change the V_{SENSE} switching threshold and adjust the output current. Details of the different modes of adjusting output current are given in the applications section by:

$$I_{OUT_DC} = \frac{V_{ADJ}}{1.25 V} \times \frac{100 mV}{R_{SENSE}}$$
 (for 0.3V < V_{ADJ} <2.5V)

The value of the output current is 1A at 0.1 Ω (0.5A at 0.2 Ω) and this is a calculated output current when the ADJ terminal is 1.25V floating.

Shutdown Mode

Taking the ADJ pin to a voltage below 0.2V for more than approximately 100us will turn off the output, and supply current will fall to a low standby level of 20uA nominal.

Soft-Start

The device has inbuilt soft-start action due to the delay through the PWM filter. An external capacitor from the ADJ pin to ground will provide additional soft-start delay, by increasing the time taken for the voltage on this pin to rise to the turn-on threshold and by slowing down the rate of rise of the control voltage at the input of the comparator.

With no external capacitor, the time taken for the output to reach 90% of its final value is approximately 500us. Adding capacitance increases this delay by approximately 0.5ms/nF.

Inherent open-circuit LED protection

If the connection to the LED(s) is open-circuited, the coil is isolated from the LX pin of the chip, so the chip will not be damaged. Unlike in many boost converters, where the back EMF may damage the internal switch by forcing the drain above its breakdown voltage.



_ Absolute Maximum Rating

(Note1)	
	0.3V to +45V
ISENSE Voltage, V _{ISENSE}	+0.3V to -5V (measured with respective to V _{IN})
LX Output Voltage, V _{LX}	0.3V to +45V
Adjust Pin Input Voltage, V _{ADJ}	
Switch Output Current, I _{LX}	1.8A
Storage Temperature Range	
Operation Temperature Range	
Lead Temperature Range (Soldering 10sec)	260°C
ESD Rating (Note2)	
	200V
HBM (Human Body Mode)	4kV
	The was all lands was a time.
	Thermal Information
Package Thermal Resistance (Note3)	I nermal information
Package Thermal Resistance (Note3) PSOP-8L θ ₁₀	
PSOP-8L θ_{JA}	47°C/W
PSOP-8L θ_{JA} PSOP-8L θ_{JC}	
PSOP-8L θ_{JA}	47°C/W 17.9°C/W
PSOP-8L θ_{JA}	47°C/W
PSOP-8L θ_{JA}	47°C/W 17.9°C/W
PSOP-8L θ_{JA}	

- **Note 1.** Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.
- Note 2. Devices are ESD sensitive. Handling precaution recommended.
- **Note 3.** θ_{JA} is measured in the natural convection at $T_A = 25^{\circ}C$ on a high effective thermal conductivity test board of JEDEC 51-7 thermal measurement standard.
- Note 4. The device is not guaranteed to function outside its operating conditions.



Electrical Characteristics

($V_{IN} = 12V$, $T_A = +25^{\circ}C$ unless otherwise specified.)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Supply Input						
Supply Voltage Range	V _{IN}		7		40	V
V _{IN} POR Threshold	V _{IN-RTH}	V _{IN} Rising.		4.95		V
V _{IN} POR Threshold	V _{INFTH}	V _{IN} Falling		4.8		V
Quiescent Current with Output Off	I _{Q-OFF}	ADJ pin grounded		20	40	uA
Quiescent Current with Output Switching	I _{QON}	ADJ pin floating f = 250kHz		0.5	1.0	mA
ISENSE Pin						
Current Sense Threshold Voltage	V _{ISENSE}	ISENSE pin with respect to VIN, ADJ = 1.25V	95	100	105	mV
ISENSE Input Current	I _{ISENSE}	V _{ISENSE} = V _{IN} - 0.1V			1.0	uA
Reference						
Reference Voltage	V_{REF}	ADJ pin voltage		1.25		V
Reference Voltage Temperature Coefficient				50		ppm /ºK
ADJ Pin						
ADJ Pin for DC Control Level	V_{ADJ}		0.3		2.5	V
ADJ to Switch Device from On State to Off State	V _{ADJ-OFF}	V _{ADJ} falling	0.15	0.2	0.25	V
ADJ to Switch Device from Off State to On State	V _{ADJ-ON}	V _{ADJ} rising	0.2	0.25	0.3	V
AD I to VDEE Desistance	R _{ADJ}	0 < V _{ADJ} < V _{REF}	135		250	kΩ
ADJ to VREF Resistance		V _{ADJ} > V _{REF} +100mV	13.5		25	kΩ
ADJ Pin Low Frequency Duty Cycle Range	D _{PWM(LF)}	PWM frequency < 500Hz	10		100	%
ADJ Pin High Frequency Duty Cycle Range	D _{PWM(HF)}	PWM frequency >10KHz	16		100	%
LX Pin				ı		ı
LX Switch Current	I _{LX-MEAN}				1.5	А
LX R _{DSON}	R _{LX}	@I _{LX} = 1.5A		0.3	0.6	Ω
LX Leakage Current					5	uA
Minimum Switch ON Time	T _{ON-MN}	LX Switch ON		240		ns
Minimum Switch OFF Time	T _{OFF-MN}	LX Switch OFF		200		ns
Recommended Minimum Switch ON Time	TONMINREC			800		ns

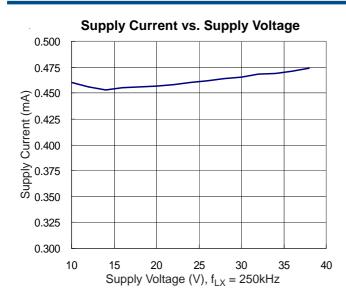


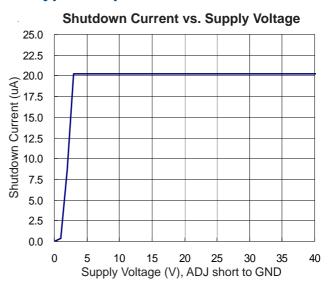
Electrical Characteristics

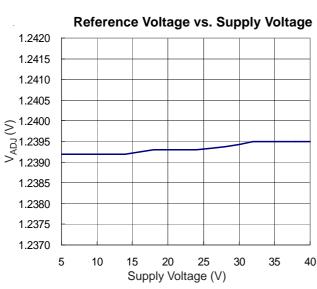
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Frequency						
Operation Frequency	f _{LX}	ADJ pin floating, L = 33uH (0.093 Ω), I _{OUT} = 1A @ V _{LED} = 3.6V driving 1LED		280		kHz
Recommended Maximum Operation Frequency	f _{LX-MAX}				1	MHz
Duty Cycle Range of Output Switch of Operation Frequency	D _{LX}		30		70	%
Internal Comparator Propagation Delay	t _{PD}			50		ns
Soft Start						
Soft Start Time	t _{SS}	Time taken for output current to reach 90% of final value after voltage on ADJ pin has risen above 0.3V.		500		us

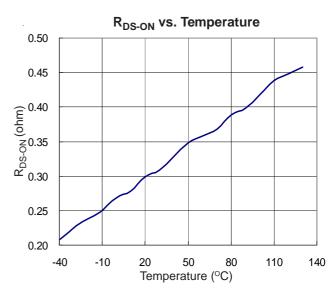


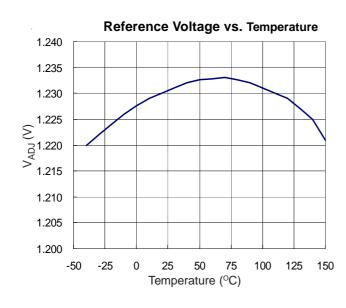
Typical Operation Characteristics

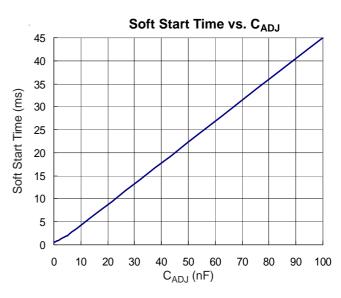






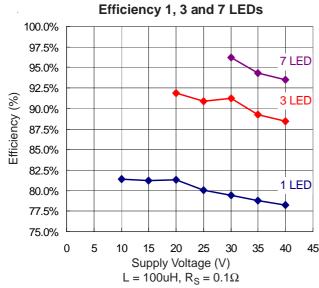


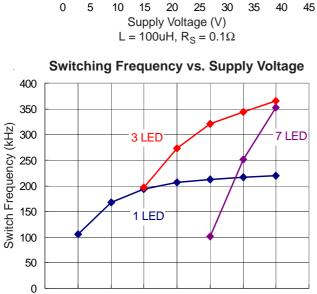






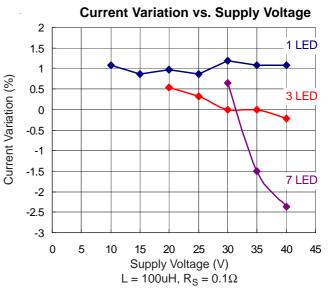
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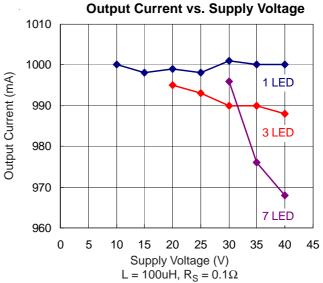




Supply Voltage (V)

L = 100uH, $R_S = 0.1\Omega$

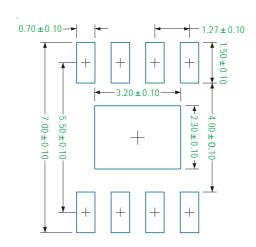


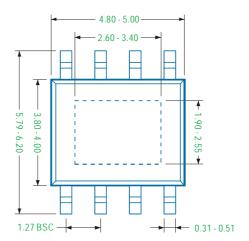




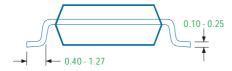
Package Information

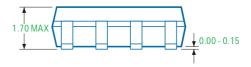
PSOP - 8L





Recommended Solder Pad Layout





Note

1. Package Outline Unit Description:

BSC: Basic. Represents theoretical exact dimension or dimension target

MIN: Minimum dimension specified.

MAX: Maximum dimension specified.

REF: Reference. Represents dimension for reference use only. This value is not a device specification.

TYP. Typical. Provided as a general value. This value is not a device specification.

- 2. Dimensions in Millimeters.
- 3. Drawing not to scale.
- 4. These dimensions do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm.