



4A, CC/CV Mode Step-Down Converter

DESCRIPTION

The EUP3467 is a step-down converter capable of driving 4A continuous load with excellent line and load regulation. The EUP3467 operates in either CC (Constant Current) mode or CV (Constant Voltage) mode with an input voltage range from 8V to 36V. The EUP3467 provides programmable cable compensation by adjusting external resistor divider. The EUP3467 stops switching when the output reaches over voltage threshold which is programmed by external resistor divider.

Fault protection includes secondary cycle-by-cycle current limit, short circuit protection and thermal shutdown. In shutdown mode the regulator draws $3\mu A$ of supply current. Internal soft-start minimizes the inrush supply current and the output overshoot at initial startup.

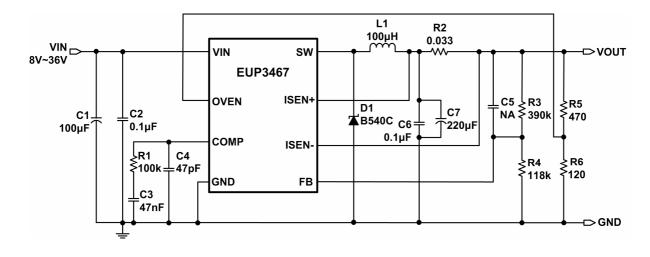
The EUP3467 is available in SOP-8 (EP) package.

FEATURES

- 8V to 36V Wide Input Operating Range
- 40V Input Voltage Surge
- Fixed 80kHz Switching Frequency
- CC/CV Mode Control
- Up to 4A Output Current
- +/- 1% Voltage Reference Accuracy
- +/- 4% Constant Current Accuracy
- Programmable Cable Compensation
- Internal Soft Start
- Output Over Voltage Protection
- Foldback Short Circuit Protection
- Secondary Cycle-by-Cycle Current Limit
- Thermal Shutdown
- Available SOP-8 (EP) Package
- RoHS Compliant and 100% Lead(Pb)-Free Halogen-Free

APPLICATIONS

- Car Charger
- Portable charger applications
- DC/DC converters with current limited



Typical Application Circuit







Pin Configurations

| Package Type | Pin Configurations | | | |
|--------------|--|--|--|--|
| SOP-8 (EP) | (Top View) GND 1 FB 2 SW ISEN- 3 ISEN+ 4 SW 6 VIN 5 SW | | | |

Pin Description

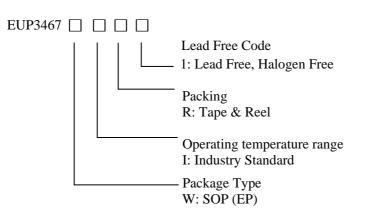
| Pin Name | SOP-8 (EP) | DESCRIPTION | | | |
|----------------|------------|--|--|--|--|
| GND | 1 | Ground. | | | |
| FB | 2 | Output Feedback Input. | | | |
| ISEN- | 3 | Current sense negative input pin. | | | |
| ISEN+ | 4 | Current sense positive input pin. | | | |
| SW | 5 | Power Switcher Output. | | | |
| VIN | 6 | Input Supply Pin. | | | |
| OVEN | 7 | OV (output over voltage) threshold setting pin. Drive OVEN voltage logic high to turn off the converter. | | | |
| COMP | 8 | Loop compensation pin. | | | |
| Thermal Pad | - | SW (Thermal Pad must be connected to the SW pin). | | | |





Ordering Information

| Order Number | Package Type | Marking | Operating Temperature Range |
|--------------|--------------|-----------------|------------------------------------|
| EUP3467WIR1 | SOP-8 (EP) | ¥xxxxx P3467 | -40°C to +85°C |



Block Diagram

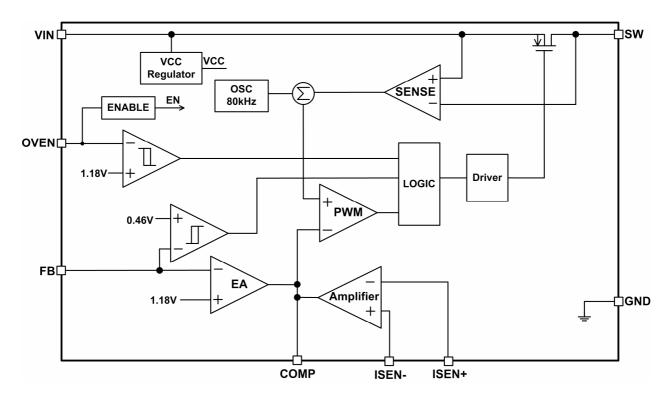


Figure 2. Functional Block Diagram





Absolute Maximum Ratings (1)

| | Input Voltage (V _{IN}) | -0.3V to 40V |
|---|---|------------------|
| | Switch Voltage (V _{SW}) | -1V to VIN +0.3V |
| | ISEN+, ISEN | -0.3V to 7V |
| | All Other Pins | -0.3V to 6V |
| | Junction Temperature | 150°C |
| | Storage Temperature | -65°C to +150°C |
| | Lead Temp(Soldering, 10sec) | 260°C |
| • | Thermal Resistance θ_{JA} (SOP-8_EP) | 60°C /W |
| | | |

Recommend Operating Conditions (2)

- Supply Voltage (V_{IN}) ------ 8V to 36V
- Operating Temperature Range ------ -40°C to +85°C

Note (1): Stress beyond those listed under "Absolute Maximum Ratings" may damage the device.

Note (2): The device is not guaranteed to function outside the recommended operating conditions.

Electrical Characteristics

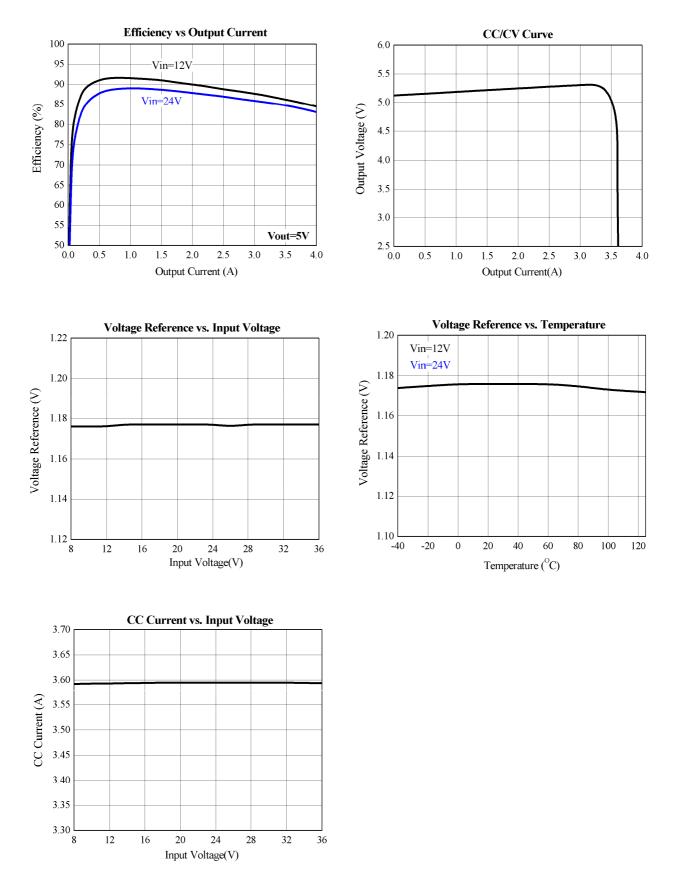
(V_{IN}=12V, T_A =+25°C, I_{OUT}=2.0A, unless otherwise specified)

| Parameter | Conditions | EUP3467 | | | Unit | |
|---|--|---------|------|-------|------|--|
| Farameter | Conditions | Min. | Тур. | Max. | Omt | |
| Input voltage | | 8 | | 36 | V | |
| Input No Load Current | I _{OUT} =0A | | | 10 | mA | |
| Quiescent Current | $V_{OVEN} = 0V, V_{FB} = 1.5V$ | | 0.5 | 1 | mA | |
| Shutdown Current | $V_{OVEN} = 5V$ | | 2.5 | | μΑ | |
| Input Under Voltage Lockout Threshold Rising | | 6.2 | 6.7 | 7.2 | V | |
| Input Under Voltage Lockout Threshold Hysteresis | | | 1 | | V | |
| Current Limit | | | 6 | | А | |
| Operating frequency | | 72 | 80 | 88 | kHz | |
| Operating frequency | T_J = -25°C to 125°C | 68 | | 92 | kHz | |
| Maximum Duty Cycle | | | | 100 | % | |
| Minimum On-Time | | | 400 | | ns | |
| Defense as Velteres of the Velteres Error Arealifier | | 1.168 | 1.18 | 1.192 | V | |
| Reference Voltage of the Voltage Error Amplifier | $T_J = -25^{\circ}C$ to $125^{\circ}C$ | 1.155 | | 1.205 | V | |
| Defense Valtere of the Comment Enner Annullifier | | 113 | 118 | 123 | mV | |
| Reference Voltage of the Current Error Amplifier | $T_J = -25^{\circ}C$ to $125^{\circ}C$ | 111 | | 125 | mV | |
| Transconductance of Error Amplifier | | | 75 | | μA/V | |
| Defense Valtere of the Oren Valtere Commenter | | 1.145 | 1.18 | 1.215 | V | |
| Reference Voltage of the Over Voltage Comparator | $T_J = -25^{\circ}C$ to $125^{\circ}C$ | 1.133 | | 1.227 | V | |
| Reference Voltage of the Short Circuit Foldback Comparator | | | 0.46 | | V | |
| OVEN Shutdown Threshold Voltage | | 2.5 | | | V | |
| Thermal Shut down Temperature | Temperature Rising | | 150 | | °C | |
| Thermal Shut-down Temperature | Temperature Falling | | 100 | | °C | |
| PMOS Drain-Source On-State Resistance | V _{IN} =24V, I _{OUT} =1A | | | 90 | mΩ | |



Typical Operating Characteristics

 V_{IN} =12V, V_{OUT} =5V, T_A =25°C, unless otherwise noted.

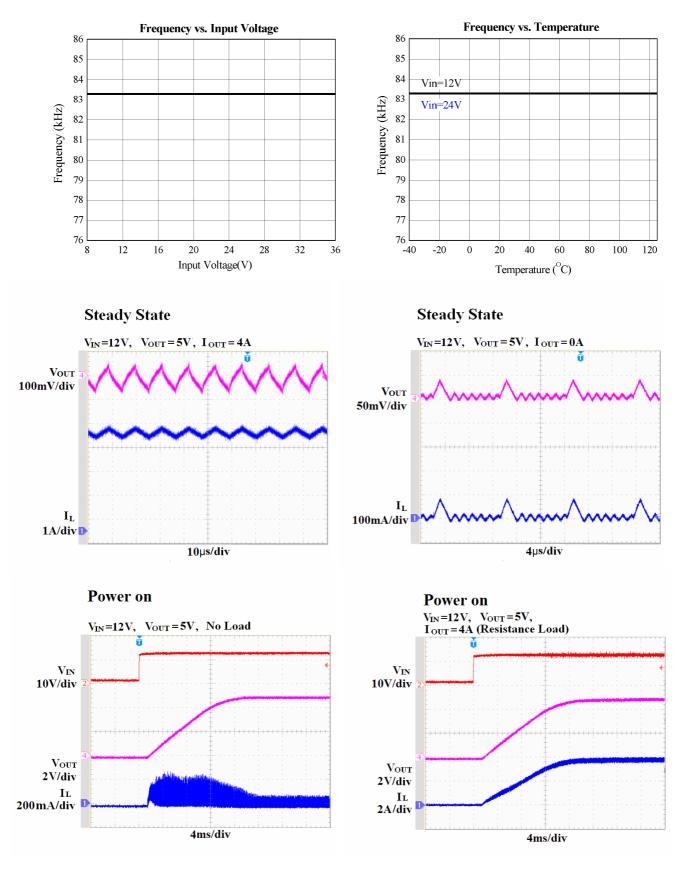






Typical Operating Characteristics (continued)

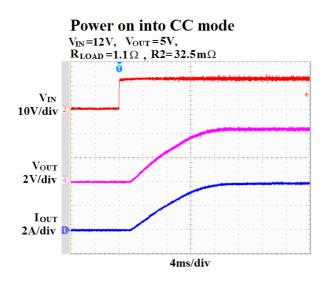
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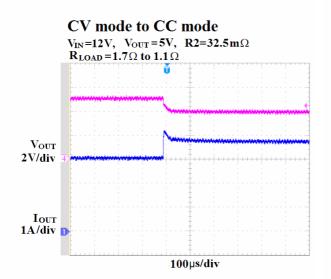




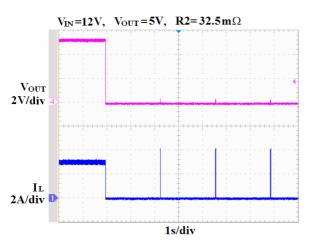
Typical Operating Characteristics (continued)

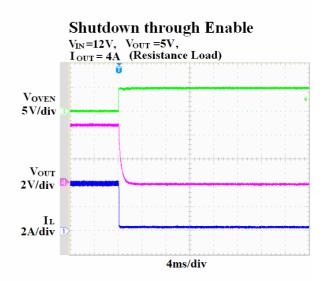
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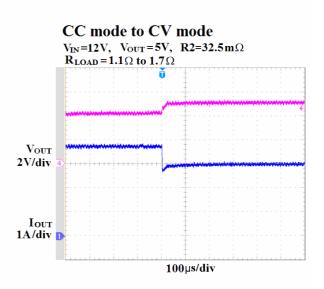




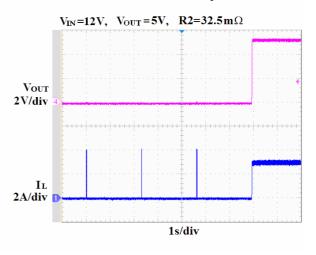
Short Circuit







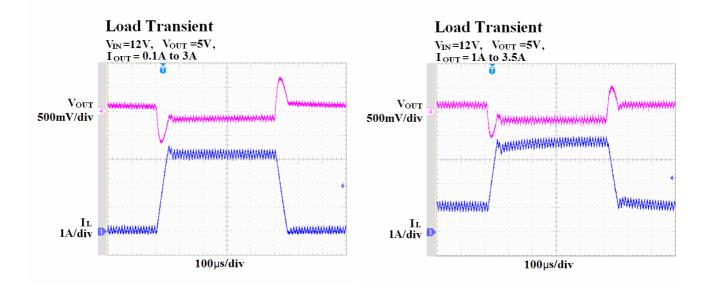
Short Circuit Recovery





Typical Operating Characteristics (continued)

 V_{IN} =12V, V_{OUT} =5V, T_A =25°C, unless otherwise noted.







Functional Description

The EUP3467 is a current-mode step-down converter with 8V to 36V input power supply. The device can provide up to 4A continuous current to the output. The EUP3467 uses current-mode architecture to control the regulator loop. The output voltage is measured at FB through a resistive voltage divider and amplified through the internal error amplifier. The output current of the transconductance error amplifier is presented at COMP pin where a RC network compensates the regulator loop. Slope compensation is internally added to eliminate subharmonic oscillation at high duty cycle. The slope compensation adds voltage ramp to the inductor current signal which reduces maximum inductor peak current at high duty cycles.

CC/CV mode control

The EUP3467 operates in either CC mode or CV mode. The CV mode regulates the output voltage. When output current reaches the CC threshold, the device enters CC mode to limit the output current.

Programmable cable compensation

The EUP3467 provides programmable cable compensation by adjusting the external resistor divider to compensate resistive voltage drop across the charger's output cable.

Internal soft-start

Internal soft-start minimizes the inrush supply current and the output overshoot at initial startup.

Over voltage protection

OVP (over voltage protection) function with programmable OV (over voltage) threshold set by the external resistor divider is provided. When output over voltage occurs, the device shuts down and returns to normal operation automatically when the output over voltage is released.

Short circuit protection

The EUP3467 provides the output short circuit protection function to prevent large output short circuit from damaging the device. When output short happens, the device shuts down and returns to normal operation automatically when the short circuit condition is released.

Thermal Shutdown

The EUP3467 stops switching when its junction temperature exceeds 150° C and resumes when the temperature has dropped by 50° C to protect the device.

Application Information

The output voltage is set through a resistive voltage divider and can be expressed by the equation as follows

$$V_{OUT} = 1.18V * (R3 + R4) / R4$$

Setting the CC current

EUP3467 constant current value is set by the resistor R2 connected between the ISEN- and ISEN+ pins. The CC current is determined by the equation as follows

$$I_{cc} = 0.118 V/R2$$

Setting the output over voltage threshold

The OVEN pin voltage is set by a resistor divider connected between VOUT and GND. When OVEN pin voltage is higher than 1.18V, the regulator stops switching until OVEN pin voltage falls below 1.18V. Thus the output over voltage threshold is

$$V_{OV} = 1.18V * (R5 + R6) / R6$$

Setting the cable compensation

The EUP3467 provides programmable cable voltage drop compensation using the impedance at the FB pin to compensate voltage drop across the charger's output cable. The cable compensation voltage can be expressed as

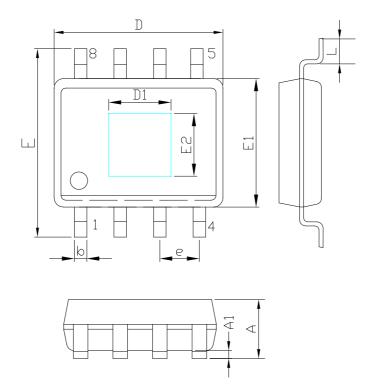
$$\Delta V_{OUT} = 5.27 \mu * I_{OUT} * R2 * R3$$

By adjust the value of R3, the cable compensation voltage can be programmed.





Packaging Information



Remark: Exposed pad outline drawing is for reference only.

| SYMBOLS | MILLIMETERS | | | INCHES | | |
|---------|-------------|--------|------|-----------|--------|-------|
| | MIN. | Normal | MAX. | MIN. | Normal | MAX. |
| А | 1.35 | - | 1.75 | 0.053 | - | 0.069 |
| A1 | 0.00 | - | 0.25 | 0.000 | - | 0.010 |
| D | 4.80 | 4.90 | 5.00 | 0.189 | 0.193 | 0.197 |
| E1 | 3.70 | 3.90 | 4.00 | 0.146 | 0.154 | 0.157 |
| D1 | 2.67 | 2.97 | 3.50 | 0.105 | 0.117 | 0.138 |
| E2 | 1.78 | 2.18 | 2.60 | 0.070 | 0.086 | 0.102 |
| Е | 5.80 | 6.00 | 6.20 | 0.228 | 0.236 | 0.244 |
| L | 0.40 | - | 1.27 | 0.016 | - | 0.050 |
| b | 0.31 | - | 0.51 | 0.012 | - | 0.020 |
| е | 1.27 REF | | | 0.050 REF | | |