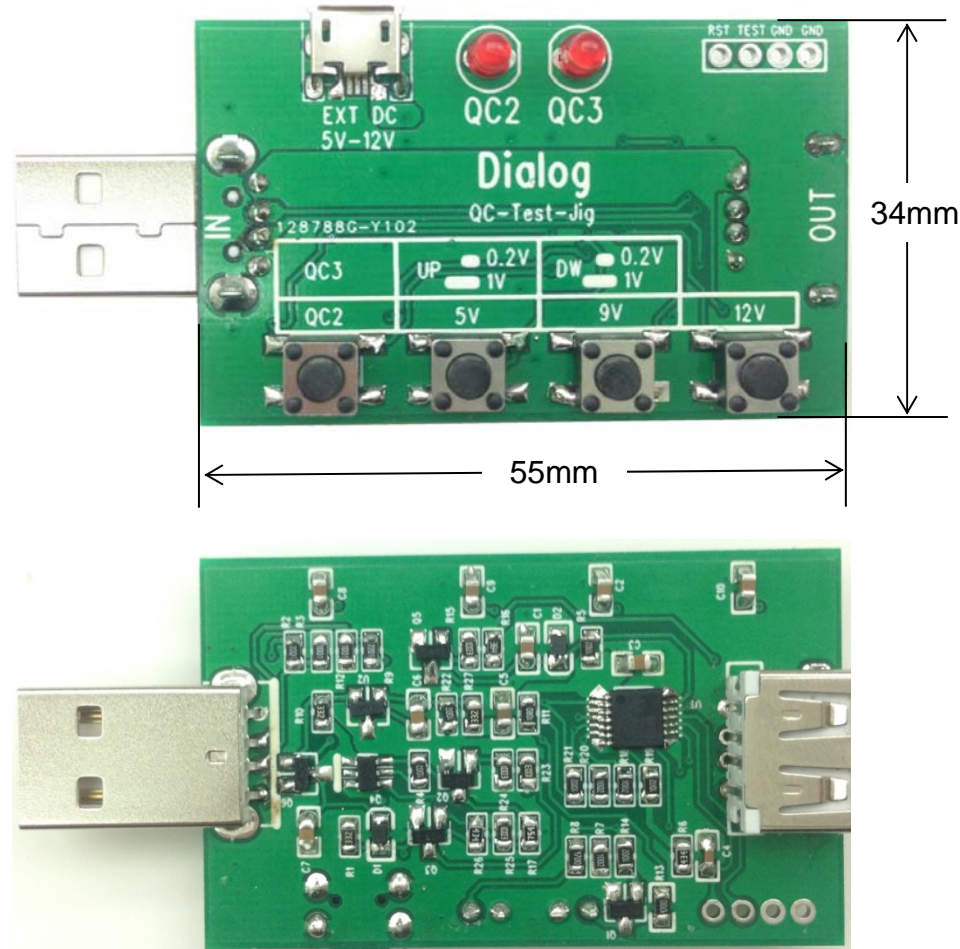


3. Circuit Board Photograph

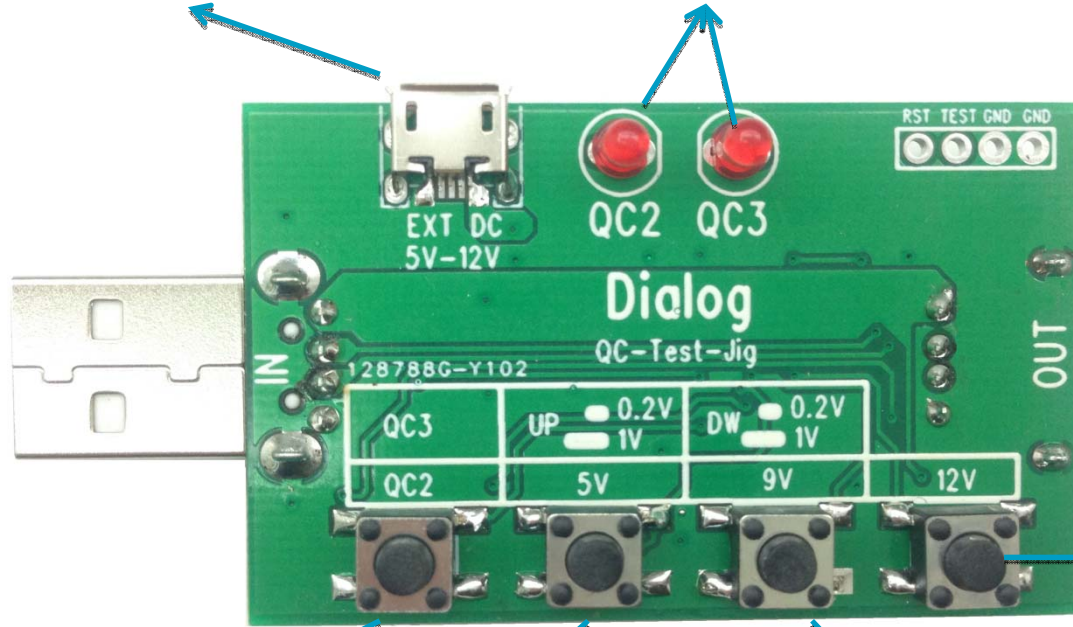


4. Description

External DC power supply port

LED Indicator

Test Port



Loading

12V Selection
Be used in QC2.0 mode, Get 12V output

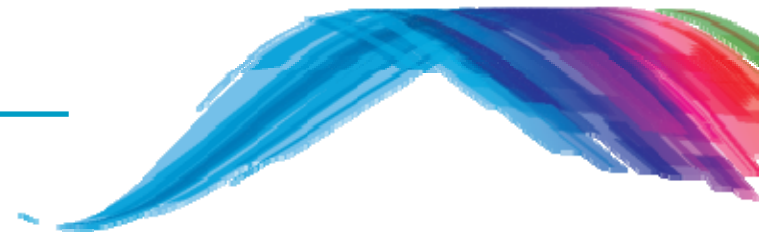
Mode Selection
Choose to QC2.0 Mode or QC3.0 Mode

5V/Up Selection
in QC2.0 mode, Get 5V output
In QC3.0 mode, Set Vout to go up with 0.2V step or 1V step

9V/DW Selection
in QC2.0 mode, Get 9V output
In QC3.0 mode, Set Vout to go down with 0.2V step or 1V step



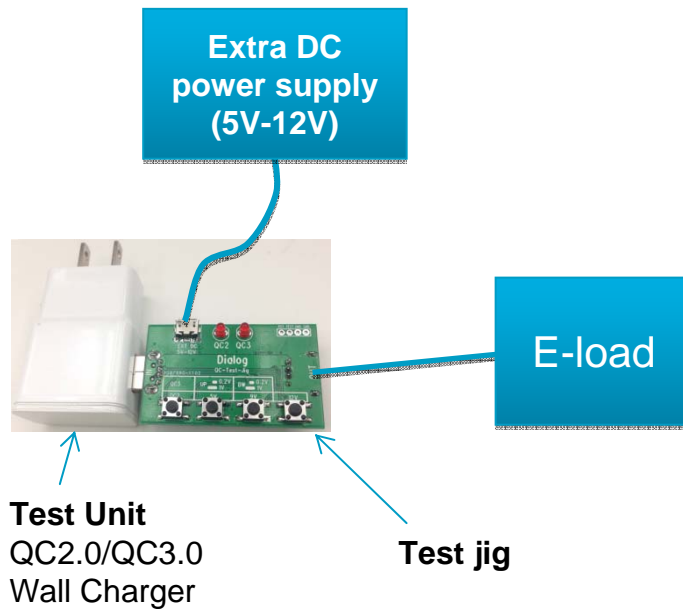
5. System Connection



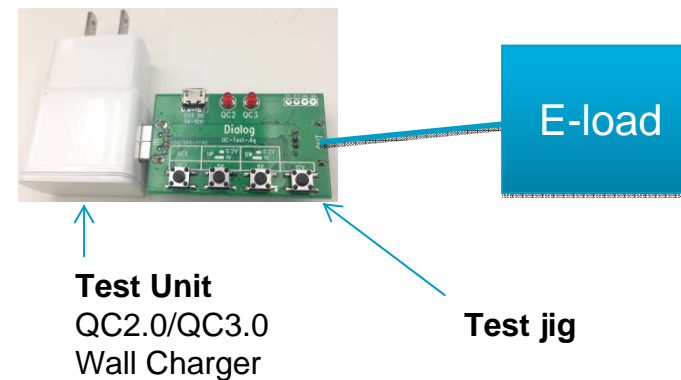
There are two ways to provide the power supply for test jig.

Solution-1: Provide the power supply for test jig by an extra DC power (The test jig would not produce additional power loss to wall charger)

Solution-2: Provide the power supply for test jig by wall charger, it is simple but the wall charger would lose some power in test jigs



Solution-1



Solution-2

6.1 Operating Guide

QC2.0 mode

Step 1. Connecting the Test Jig as Page 5.

Step 2. Switch “MD Selection” to QC2.0 Mode, the LED of QC2 will be lighted

Step 3. Press a time on “5V”, “9V” or “12V” button, will get this output voltage. For example, press a time on “9V” button, output will be set to 9V



6.2 Operating Guide

QC3.0 mode

Step 1. Connecting the Test Jig as Page 5.

Step 2. Switch “MD Selection” to QC3.0

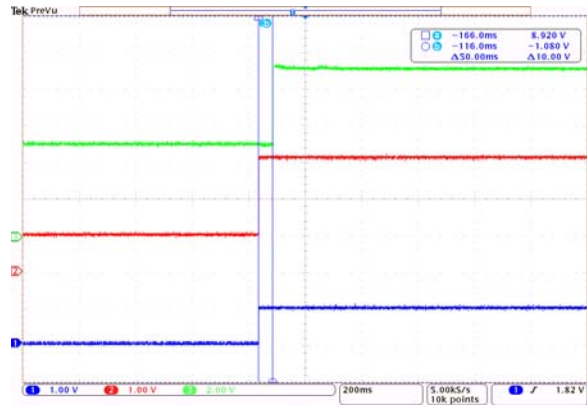
Step 3. Press a time on “UP” or “DW” button. “UP” is that output voltage is gone up with 0.2V or 1V step. “DW” is that output voltage is gone down with 0.2V or 1V step. Choose 0.2V step or 1V step by the pressed time. If the time is less than 1S, will be 0.2V step . If the time is more than 3S, will be 1V step



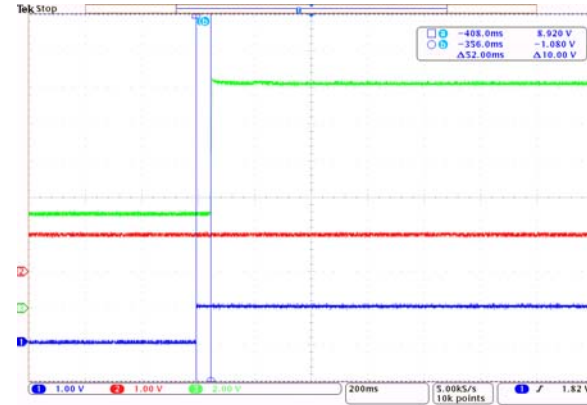
7.1 Test Waveforms for QC2.0



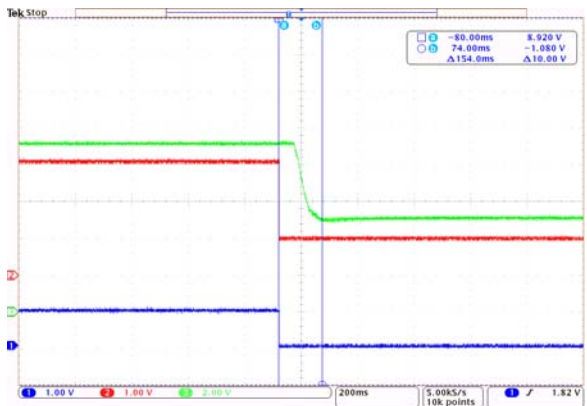
5V→9V



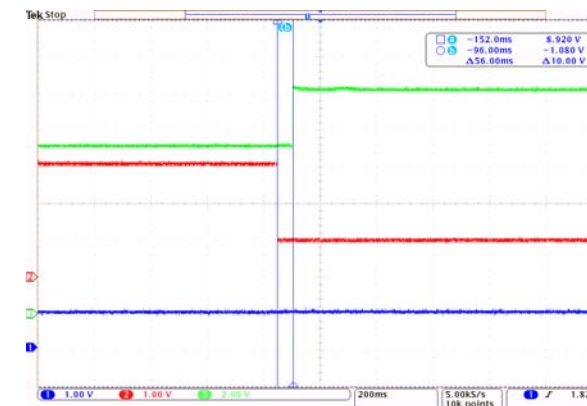
5V→12V



9V→5V



9V→12V



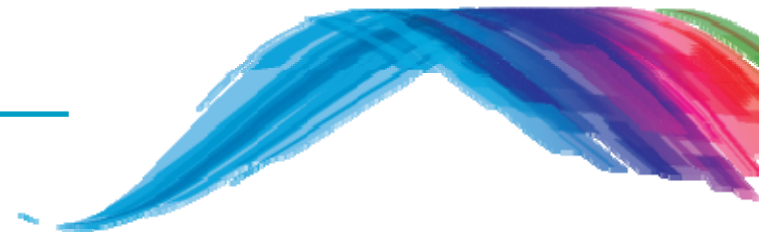
CH1: D-, 1V/Div

CH2: D+, 1V/Div

CH3: V_{OUT}, 2V/Div

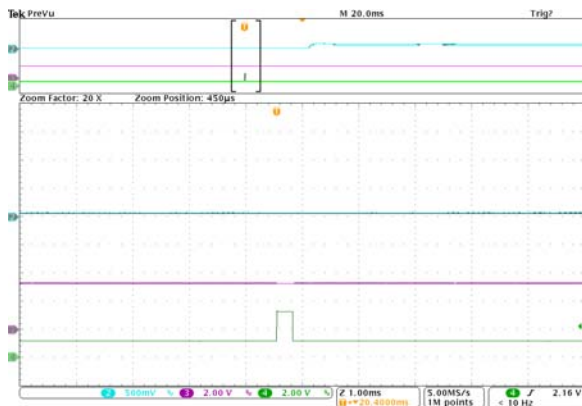
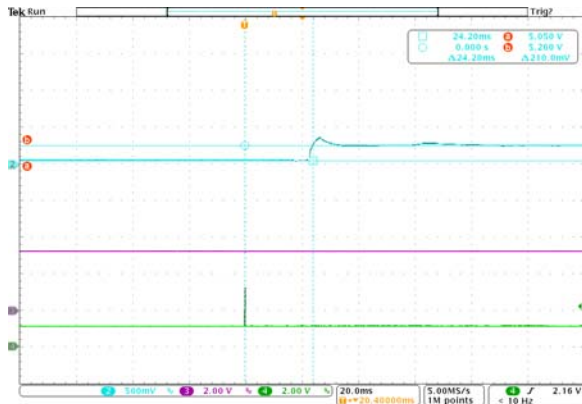


7.2 Test Waveforms for QC3.0

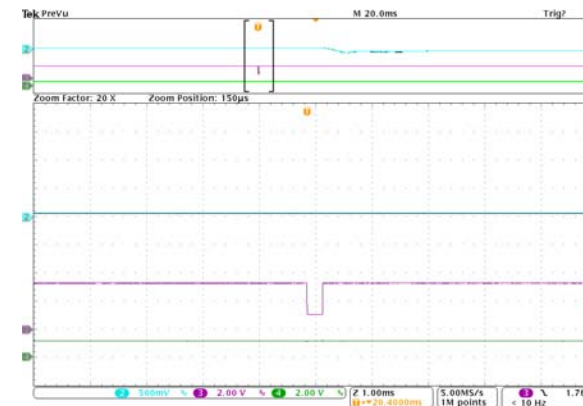
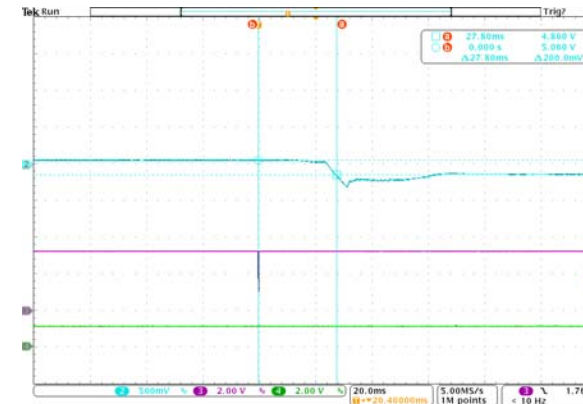


Single request mode

5V→5.2V, $T_T = 24.2\text{ms}$



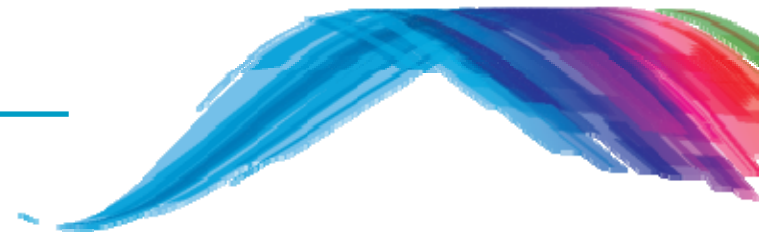
5V→4.8V, $T_T = 27.8\text{ms}$



CH2:V_{OUT}, 0.5V/Div, with 5V offset; CH3: D-, 2V/Div ; CH4:D+, 2V/Div;

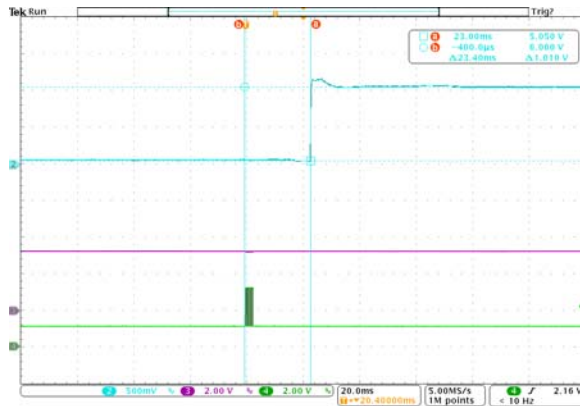


7.3 Test Waveforms for QC3.0

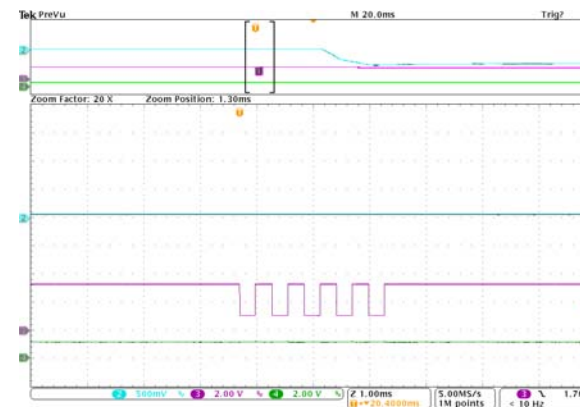
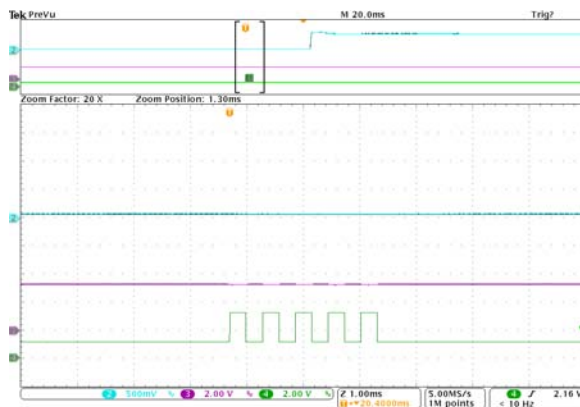
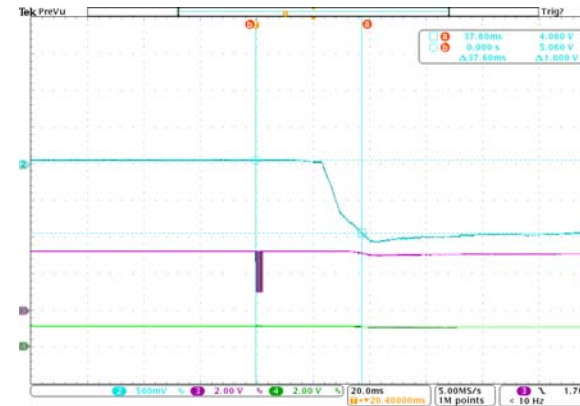


Group request mode

5V→6V



5V→4V



CH2:V_{OUT}, 0.5V/Div, with 5V offset; CH3: D-, 2V/Div ; CH4:D+, 2V/Div;



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