

Description

The Flicker-Free[™] IS31LT3935 is a single stage current mode LED driver optimized for high power factor and compatibility with all TRIAC dimmers. The PFC architecture enables excellent power factor over a wide range of operating line and load conditions, even with the simplest of inductor based driver topologies, thereby reducing system cost and size while maximizing efficiency.

The IS31LT3935 LED controller features patent pending AccuDim[™] flicker-free dimming technology that mimics the characteristics of an incandescent light bulb. It presents a dynamic impedance to the dimmer and integrates an active bleed circuit for true dimming performance across all dimmers.

The device is available in a tiny 10 lead DFN-EP (3mm \times 3mm) package. It operates over the temperature range of -40°C to +85°C.

Features

- Smooth 0-100% Flicker-free Dimming Range
- Compatibility with all TRIAC Dimmers (Digital, Leading and Trailing-edge)
- Near unity PFC without External PFC Circuitry
- Spread Spectrum Switching for Reduced EMI
- Low 500µA Quiescent Current
- Protections:
 - Soft Start
 - Under-voltage, (Over-voltage) Lockout
 - Thermal Shutdown

Applications

- Dimmable Retrofit LED Lamps and Luminaries 8 to 30W
- Industrial and Commercial Lighting
- Offline LED Driver Modules and Bricks

Quick Start

Recommended Equipment

- 85~265VAC/50~60Hz power supply
- LED array(12 in series)40Vdc-0.35A
- 220V input TRIAC Dimmer

WARNING: Operating this demo board at output voltages other than the designed output target voltage may reduce system performance, and in some cases may damage the IC or other circuit components.

Absolute Maximum Ratings

≤ 150VAC power supply
≤ 47V Vout (Total Vf)

Caution: Do not exceed the conditions listed above, otherwise the board will be damaged or the output will be limited

Procedure

The IS31LT3935 DEMO Board is fully assembled and tested. Follow the steps listed below to verify board operation.

Caution: Do not turn on the power supply until all connections are completed.

- Connect the positive terminal of the LEDs to the LED+ of the DEMO and the negative terminal of the LEDs to the LED- of the Evaluation Board.
- Connect the input pin N of the Evaluation Board via the main power switch to AC power supply N.
- Connect the input pin L of the Evaluation Board via TRAIC Dimmer pin L, Another pin of TRAIC Dimmer Connect to the AC power supply L.
- Turn on the power supply, Adjust the angle of TRAIC Dimmer



Ordering Information

PART#	ТЕМР	IC	
	RANGE	PACKAGE	
	40 to 125°C	DFN-10	
1331L13933-DL32-TR	-40 to 125 C	(Exposed Pad)	

For pricing, delivery, and ordering information, please contact ISSI at analog_mkt@issi.com or call +1-408-969-6600





Figure 1 IS31LT3935 Evaluation Board Schematic Note: ISSI Evaluation

Board does not include a LED array



Figure 2 Picture of Evaluation Board

NOTE: Physical dimensions are (LxWxH): 73mmx25mmx23mm

PCB Layout



Figure 3 PCB Layout – Top layer





Figure 4 PCB Layout – Bottom layer

Bill of Materials

No.	Name	Description	Ref Des.	Qty.	Mfr P/N
1	FUSE	1A250V Φ3 F1		1	
2	MOV	7D471 PIN7	VR	1	
3	Bridge rectifier	DB107 DIP4	BD1	1	
4	Inductor	6mH 0810	L1, L2	2	
5	Inductor	10mH 0810	L3	1	
6	Transformer	450uH PQ2016	TR	1	
7	Resistor	5.1kΩ 1% 0805	R1, R4, R4.1	3	
8	Resistor	510Ω 1% 1W	R2	1	
9	Resistor	160kΩ 1% 1206	R6	1	
10	Resistor	150kΩ 1% 1206	R12	1	
11	Resistor	5.1kΩ 1% 0805	R16	1	
12	Resistor	270kΩ 1% 1206	R7	1	
13	Resistor	200kΩ 1% 1206	R13	1	
14	Resistor	2kΩ 1% 0805	R17	1	
15	Resistor	10Ω 1% 1206	R3	1	
16	Resistor	30Ω 1% 1206	R14	1	
17	Resistor	110kΩ 1% 1206	R8	1	
18	Resistor	10kΩ 1% 0805	R9	1	
19	Resistor	1.0Ω 1% 0805	R15	1	
20	Resistor	100Ω 1% 0805	R10	1	
21	Resistor	240kΩ 1% 0805	R11	1	
22	Resistor	68kΩ 1% 1206	R5	1	
23	Resistor	0.18Ω 1% 1206	R18	1	
24	Resistor	NC	R27	1	
25	Resistor	300kΩ 1% 1206	R21, R22	2	
26	Resistor	180kΩ 1% 0805	R19	1	
27	Resistor	51Ω 1% 0805	R25	1	

Evoluction Board Cuida 15

221L13	SS BUID LIGHTING	Evaluation board Guide			
28	Resistor	10ΚΩ 1% 0805	R23	1	
29	Resistor	0Ω 0805	R24	1	
30	CBB-CAP	47nF 630V pin10	C1	1	
31	CBB-CAP	220nF 630V pin15	C5	1	
32	E-CAP	330uF 50V 1020	C2,C3	2	
33	SMD-CAP	22nF 25V X7R 0805	C6	1	
34	SMD-CAP	1uF 25V X7R 0805	C7,C8	2	
35	SMD-CAP	10uF 25V X7R 1206	C9,C10	2	
36	SMD-CAP	33PF 25V X7R 0603	C11,C14	2	
37	SMD-CAP	1nF 1000V X7R 1206	C13	1	
38	Y1-CAP	1nF PIN10	CY	1	
39	SMD-DIODE	ES2G	D1	2	
40	SMD-DIODE	FR107	D6	1	
41	SMD-DIODE	1N914BWT SOD-523F	D2,D4,D5	3	
42	SMD-ZENER	9.1V 1% SOD-80	D3	1	
43	SMD-ZENER	47V 1% SOD-80	D8	1	
44	MOS	NDS7002 SOT-23	Q4	1	
45	MOS	AOD4S60 TO-252	Q3	1	
46	MOS	BSP125 SOT223	Q1	1	
47	IC	IS31LT3935 DFN10	U1	1	
48	Photo-coupler	EL357 MSOP-4	U2	1	
49		NC	R20, C4,C12	0	
50	SMD-CAP	1nF1kV 10% X7R 1206	СХ	2	
51	SMD-CAP	1nF1kV 10% X7R 1206	C1.1	2	

NOTE: Please ensure that the Dimmer specifications are suitable for the application voltage and frequency.

4



Transformer Design

ELECTRICAL SPECIFCATIONS:

- 1.Primary inductance(Lp)=450uH@10kHz
- 2. Primary Leakage Inductance (Lk)< = 20uH @10KHz
- 3. Electrical Strength = 3KV, 50/60Hz,1Min

MATERIALS:

- 1. Core:PQ2016(Ferrite Material TDK PC40 or equivalent)
- 2. Bobin:PQ2016 Primary 6,Secondary:8
- 3.. Magnet Wires (Pri) : Type 2-UEW
- 4. Magnet Wire (Sec) : Triple Insulated Wires
- 5. Layer Insulation Tape :3M1298 or equivalent.

SCHEMTIC



NO.	winding	Start	End	number	number	diameter	number of	tape	remar
				of turns	of plies		plies		ks
1	NP1	3	2	17T	2	0.40mm-2-UEW	2	0.02*7.5mm	
2	NS	14	9	16T	2	2P*0.20mm-TEX	3	0.02*7.5mm	
3	NP2	2	1	16T	1	0.40mm- 2-UEW	2	0.02*7.5mm	

Line Regulation and Efficiency

Input Voltage	Input Power	THD	PF	Output Voltage	Output Current	Efficiency
100Vac/60Hz	12.14	16.8%	0.984	40.90	0.239	80.56%
110 Vac/60Hz	12.58	18.6%	0.969	40.60	0.257	82.91%
120 Vac/60Hz	12.88	19.9%	0.953	40.40	0.272	85.33%

Load Regulation and Efficiency

Input Voltage	Input Power	THD PF		Output Voltage	Output Current	Efficiency
110 Vac/60Hz	12.46	23.8%	0.969	36.43	0.283	82.70%
110 Vac/60Hz	12.58	18.6%	0.969	40.60	0.257	82.91%



EMI test Report



EMI TEST REPORT

Figure 5. L line



EMI TEST REPORT

Figure 6. N line

Copyright © 2011 Integrated Silicon Solution, Inc. All rights reserved. ISSI reserves the right to make changes to this specification and its products at any time without notice. ISSI assumes no liability arising out of the application or use of any information, products or services described herein. Customers are advised to obtain the latest version of this device specification before relying on any published information and before placing orders for products.

Integrated Silicon Solution, Inc. does not recommend the use of any of its products in life support applications where the failure or malfunction of the product can reasonably be expected to cause failure of the life support system or to significantly affect its safety or effectiveness. Products are not authorized for use in such applications unless Integrated Silicon Solution, Inc. receives written assurance to its satisfaction, that:

a.) the risk of injury or damage has been minimized;

b.) the user assume all such risks; and

c.) potential liability of Integrated Silicon Solution, Inc is adequately protected under the circumst