

```

void c_int00(void)
{
    main();
}

```

```

void main()
{

```

```

1 global_disable(); GLBEN暫存器寫入300關閉DPWM0、DPWM1

```

```

2 rom_back_door(); → clear_integrity_word(); → swi_single_entry(block.0.0.12)
                    → watchdog_reset();

```

```

3 restore_default_all(); → calculate_dflash_checksum
                        → memcpy

```

```

4 init_variables(); → configure_vin_on_off_thresholds(); → qnote_linear11_multiply_fit ( )
                  → configure_fault_levels(); → qnote_linear16_multiply_fit
                  → configure_warning_levels(); → qnote_linear11_multiply_fit
                  → configure_pgood_levels(); → qnote_linear16_multiply_fit
                  → configure_cs_limits(); → qnote_linear16_multiply_fit
                  → configure_vout_timing(); → linear11_to_qnote
                  → qnote_to_linear11
                  → qnote_scale_int32
                  → qnote_linear11_multiply_fit
                  → qnote_linear16_multiply_fit
                  → configure_cs_limits()
                  → linear11_to_qnote

```

```

5 init_gpio();

```

```

6 init_uart0();
  init_uart1();

```

```

7 init_dpwms(); → init_dpwm0();
                → init_dpwm1();
                → init_dpwm2();
                → init_dpwm3();
                → configure_dpwm_timing();

```

```

8 init_sample_trigger();

```

```

9 init_loop_mux(); → configure_cc_dac_value(); → qnote_linear11_multiply_fit

```

```

10 init_front_end0();

```

```

11 init_front_end1();

```

```

12 init_filter0(); → copy_coefficients_to_filter
  init_filter1(); → copy_coefficients_to_filter
  init_filter2();

```

```

13 init_adc12(); 實體腳位與量測順序定義

```

```

14 init_ovp();

```

```

15 init_sec_ocp(); → configure_iout_ocp(); → qnote_linear11_multiply_fit

```

```

16 init_ipri_cycle_by_cycle();

```

```

17 look_for_interrupted_dflash_erase(); → calculate_dflash_checksum
                                        → start_erase_task
                                        → calc_flash_segments
                                        → erase_dflash_segment_no_delay → swi_single_entry(segment.0.0.1)

```

```

18 init_dcomp();

```

```

19 transition_to_idle_state(); → burst_mode_disable();
                              → gpio_dpwm_off();
                              → stop_filter_states();
                              → start_up_reset();
                              → init_filter0_states();
                              → init_filter1_states();
                              → disable_current_sharing();
                              → clear_faults();
                              → global_disable()
                              → global_enable()
                              → configure_new_compensation
                              → configure_ipri_cycle_by_cycle → qnote_linear11_multiply_fit

```

```

20 global_enable();

```

```

21 init_pmbus();

```

```

22 init_interrupt(); → disable_interrupt(); → swi_single_entry(0.0.0.7)
                   → disable_fast_interrupt(); → swi_single_entry(0.0.0.5)
                   → write_reqmask → swi_single_entry(value.0.0.9)
                   → write_firqpr → swi_single_entry(value.0.0.8)
                   → enable_fast_interrupt(); → swi_single_entry(0.0.0.4)
                   → enable_interrupt(); → swi_single_entry(0.0.0.6)

```

```

for(;;)
{

```

```

23 erase_task(); → erase_dflash_segment_no_delay → swi_single_entry(segment.0.0.1)

```

```

24 pmbus_handler(); → pmbus_idle_handler(); → pmbus_write_message()
                  → pmbus_read_message()
                  → pmbus_write_block_handler(); → pmbus_write_message()
                  → pmbus_read_block_handler();
                  → pmbus_read_wait_for_eom_handler();
                  → pmbus_idle_handler(); → pmbus_write_message()
                  → pmbus_read_message()

```

```

25 uart_transmit_data(); → translate_raw_to_text(); → translate_nybble_out

```

```

26 uart_process_rx_data();

```

```

}

```