

GENERAL DESCRIPTION

The PC817 contains a light emitting diode optically coupled to a phototransistor. It is packaged in a 4-pin DIP package. Input-output isolation voltage is 5000Vrms. Response time, tr, is typically 4 μ s and minimum CTR is 50% at input current of 5mA.

- High isolation voltage between input and output (Viso=5000 V rms)
- Minimum BVCEO of 70V guaranteed
- Compact dual-in-line package

APPLICATIONS

- Feedback circuit in power supply
- System appliances, measuring instruments
- Registers, copiers, automatic vending machines
- Electric home appliances, such as fan heaters, etc.

FEATURES

- Current transfer ratio (CTR:MIN.50% at IF =5mA ,VCE =5V)

ORDERING INFORMATION

PACKAGE	TEMPERATURE RANGE	ORDERING PART NUMBER	TRANSPORT MEDIA	MARKING
PDIP-4	-30°C to +100°C	PC817x	Tube 50 units	PC817x YYWW

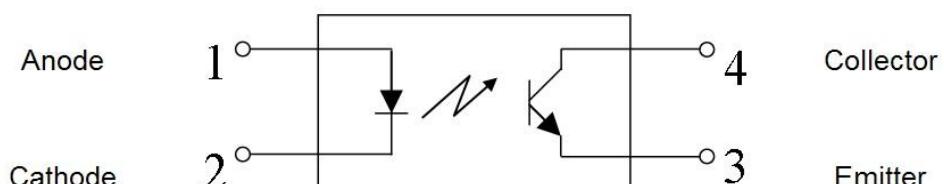
Note: "YY" represents the year; "WW" on behalf of the week; "X" represents the product of file: A or B or C or D or L or -.

Rank Table of CTR

Classification	PC817A	PC817B	PC817C	PC817D	PC817L	-
CTR	80~160	130~260	200~400	300~600	50~100	50~600

TYPICAL APPLICATION CIRCUIT

PIN ASSIGNMENT



PC817x (DIP-4)

PIN DESCRIPTIONS

Names	Pin No.	Description
Anode	1	Input Anode
Cathode	2	Input Cathode
Emitter	3	Output Emitter
Collector	4	Output Collector

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Symbol	Parameters		Value
I _F	Input	Forward Current	50mA
V _R		Reverse Voltage	6V
P		Power Dissipation	70mW
P _C	Output	Collector Power Dissipation	150mW
I _C		Collector Current	50mA
V _{CEO}		Collector-Emitter Voltage	35V
V _{ECO}		Emitter-Collector Voltage	6V
P _{tot}	Total Power Dissipation		200mW
V _{iso}	Isolation Voltage (Note 1)		5000V _{rms}
T _{stg}	Storage Temperature		-55~+125°C
T _{sol}	Soldering Temperature (Note 2)		260°C

Note 1: AC for 1 minute, R.H. = 40 ~ 60%

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector, emitter and base on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.

Note 2: For 10 seconds.

OPERATING RANGE

Symbol	Parameters	Value
Topr	Operating Temperature	-30~+100°C

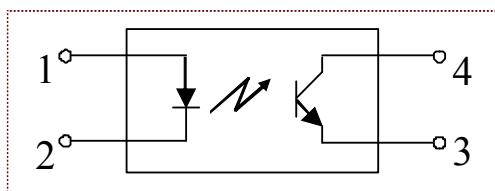
ELECTRICAL CHARACTERISTICS

($T_A=25^\circ\text{C}$, Unless Otherwise Noted.)

Symbol	Parameters	Conditions	Min.	Typ.	Max.	Units
V_F	Input	Forward	$I_F=20\text{mA}$	-	1.2	1.4
I_R		Reverse Current	$V_R=4\text{V}$	-	-	μA
C_t		Terminal Capacitance	$V=0, f=1\text{kHz}$	-	30	pF
I_{CEO}	Output	Collector Dark Current	$V_{CE}=20\text{V}$	-	-	nA
BV_{CEO}		Collector-Emitter Breakdown Voltage	$I_C=0.1\text{mA}, I_F=0$	70	-	-
BV_{ECO}		Emitter-Collector Breakdown Voltage	$I_E=10\mu\text{A}, I_F=0$	6	-	-
CTR (Note 3)	Transfer Characteristics	Current Transfer Ratio	$I_F=5\text{mA}, V_{CE}=5\text{V}$	50	-	600
$V_{CE(\text{sat})}$		Collector-Emitter Saturation Voltage	$I_F=20\text{mA}, I_C=1\text{mA}$	-	0.1	0.2
R_{iso}		Isolation Resistance	$\text{DC}500\text{V}, 40\sim60\%\text{R.H.}$	5×10^{10}	1×10^{11}	-
C_f		Floating Capacitance	$V=0, f=1\text{MHz}$	-	0.6	1.0
F_c		Cut-off Frequency	$V_{CE}=5\text{V}, I_C=2\text{mA}, R_L=100\Omega, -3\text{dB}$	-	80	-
T_r		Rise Time	$V_{CE}=2\text{V}, I_C=2\text{mA}, R_L=100\Omega$	-	4	18
T_f		Fall Time	$V_{CE}=2\text{V}, I_C=2\text{mA}, R_L=100\Omega$	-	3	18

Note 3: $\text{CTR} = I_C/I_F \times 100\%$

SIMPLIFIED BLOCK DIAGRAM



Electrical/Optical Characteristics Condition

Fig.1 Forward Current vs. Ambient Temperature

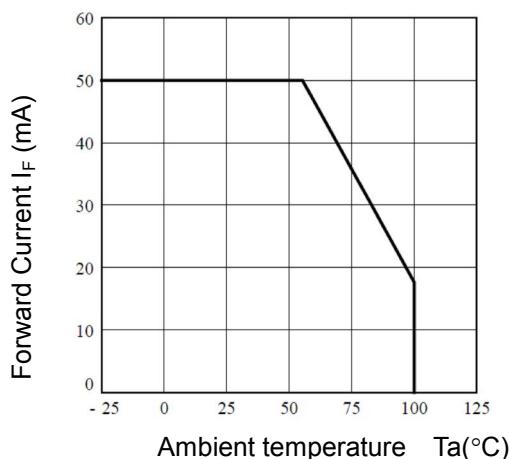


Fig.3 Forward Current vs. Ambient Temperature

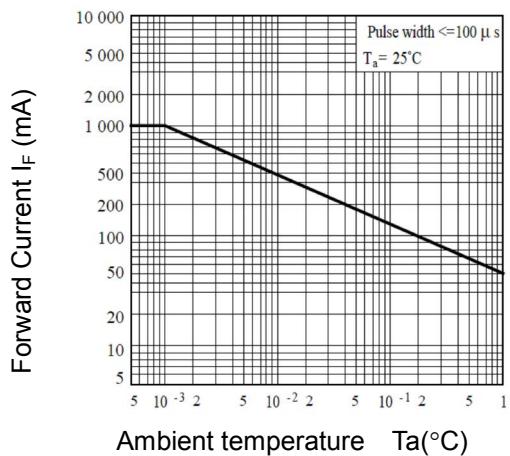


Fig.5 Forward Current vs. Forward Voltage

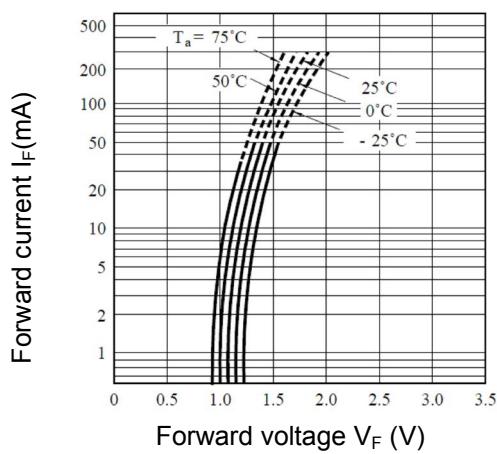


Fig.2 Collector Power Dissipation vs Ambient Temperature

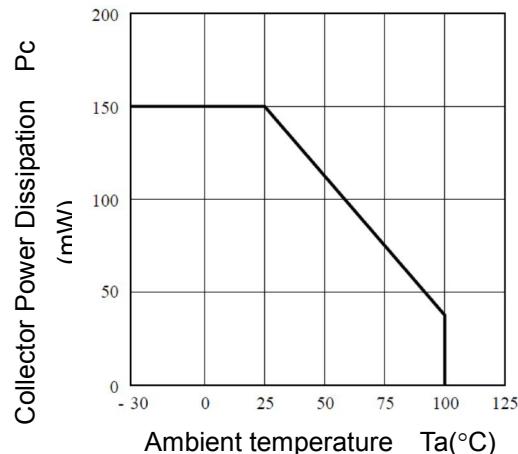


Fig.4 Current Transfer Ratio vs. Forward Current

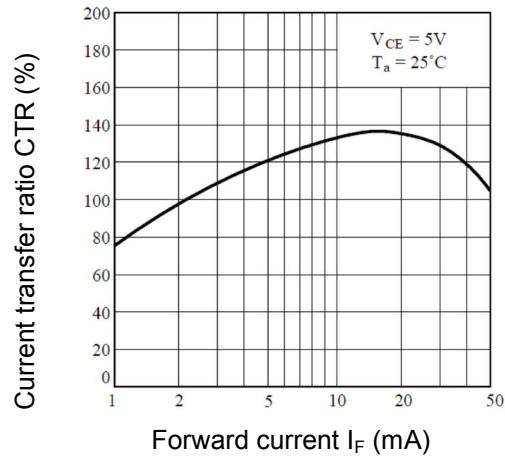


Fig.6 Collector Current vs. Collector-emitter Voltage

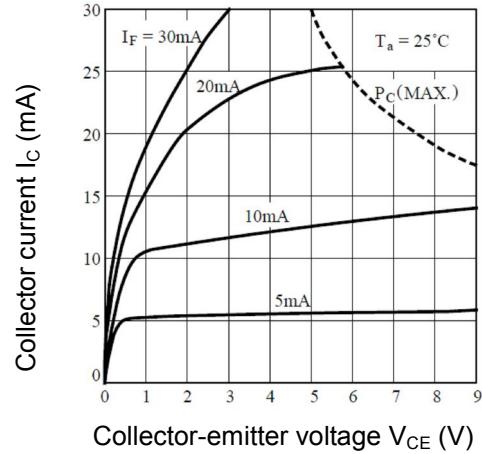


Fig.7 Relative Current Transfer Ratio vs. Ambient Temperature

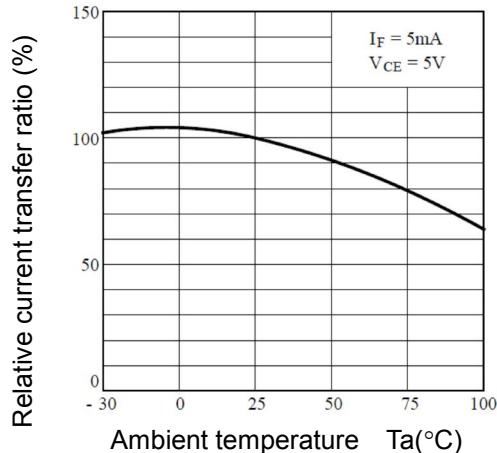


Fig.9 Collector Dark Current vs. Ambient Temperature

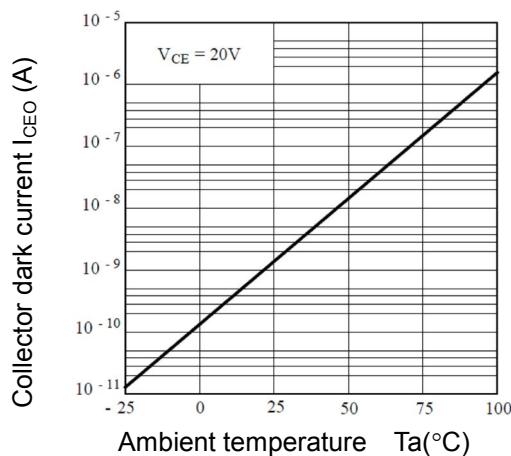


Fig.11 Frequency Response

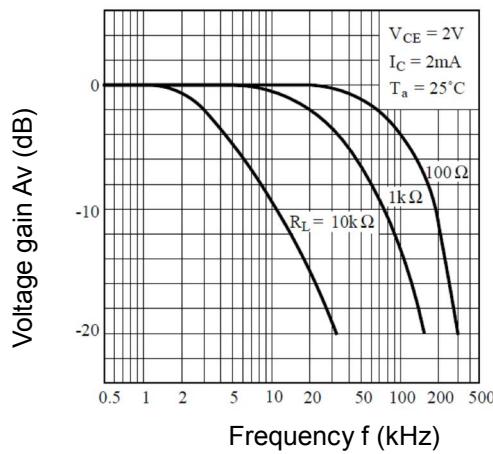


Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature

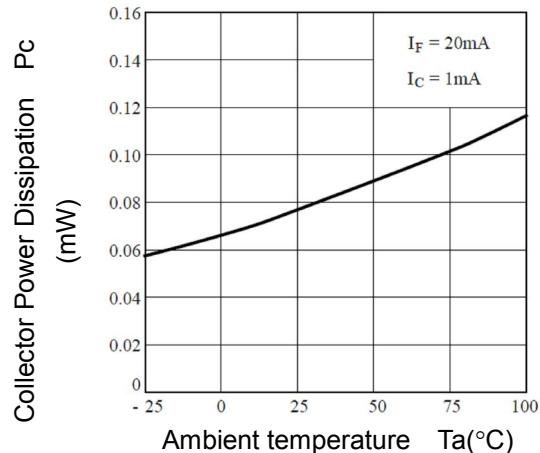
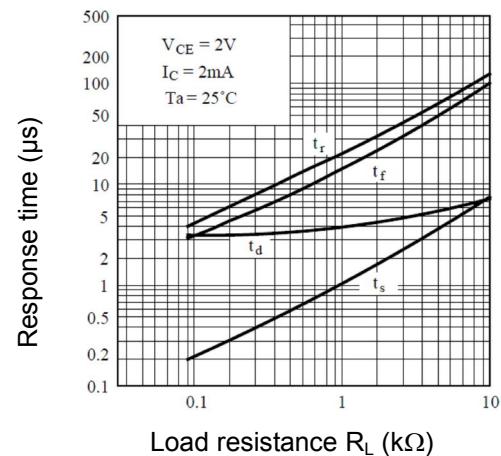
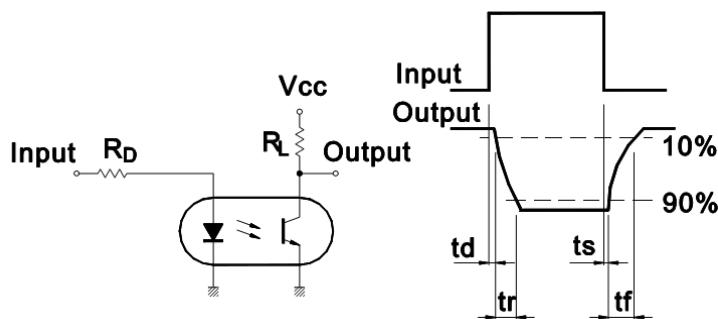


Fig.10 Response Time vs. Load Resistance

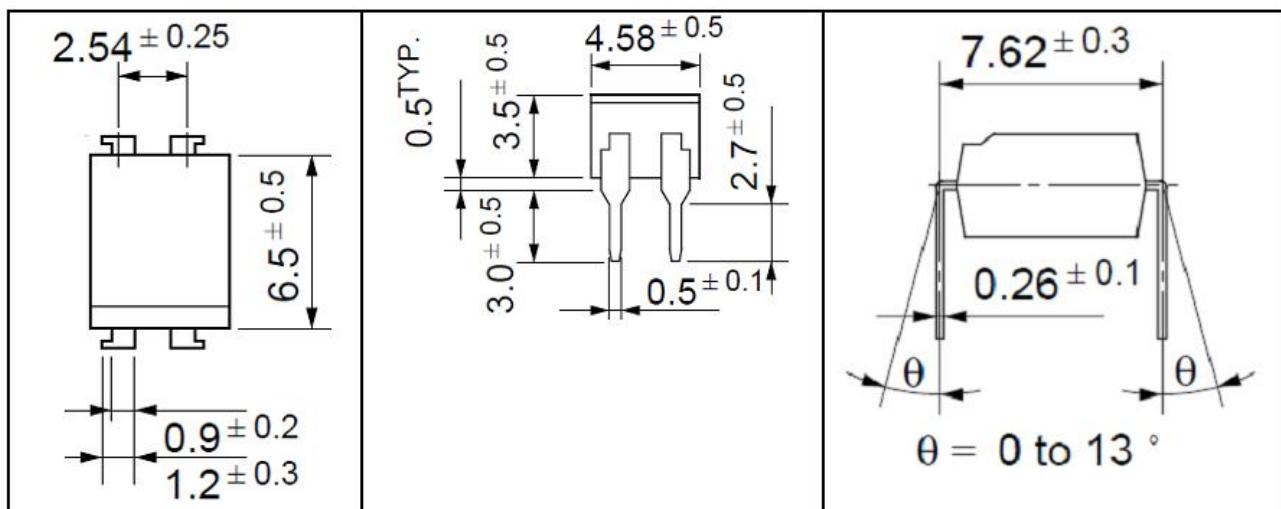




Test circuit for response time

PACKAGE INFORMATION

Outline Dimensions



4-pin DIP